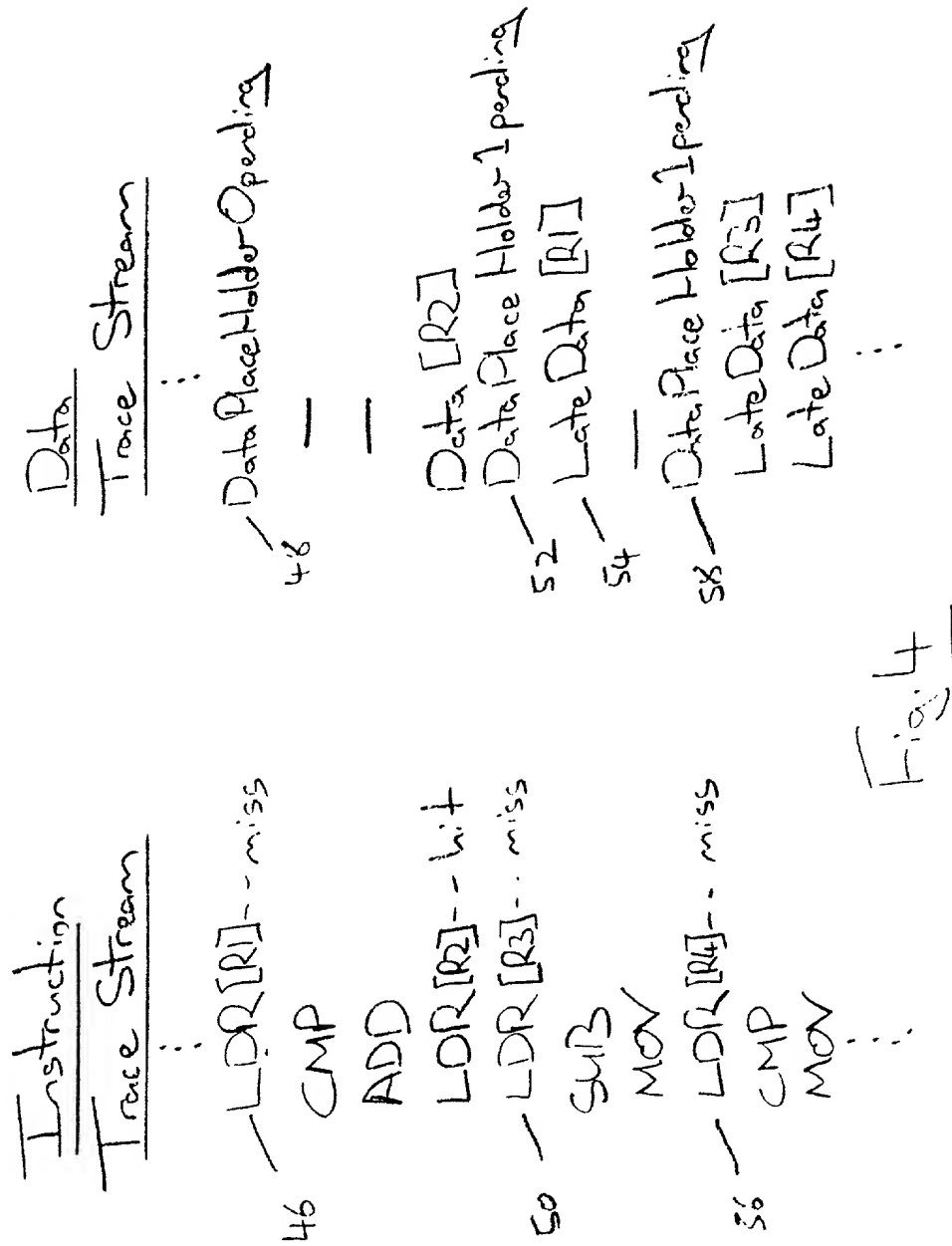


Fig. 1



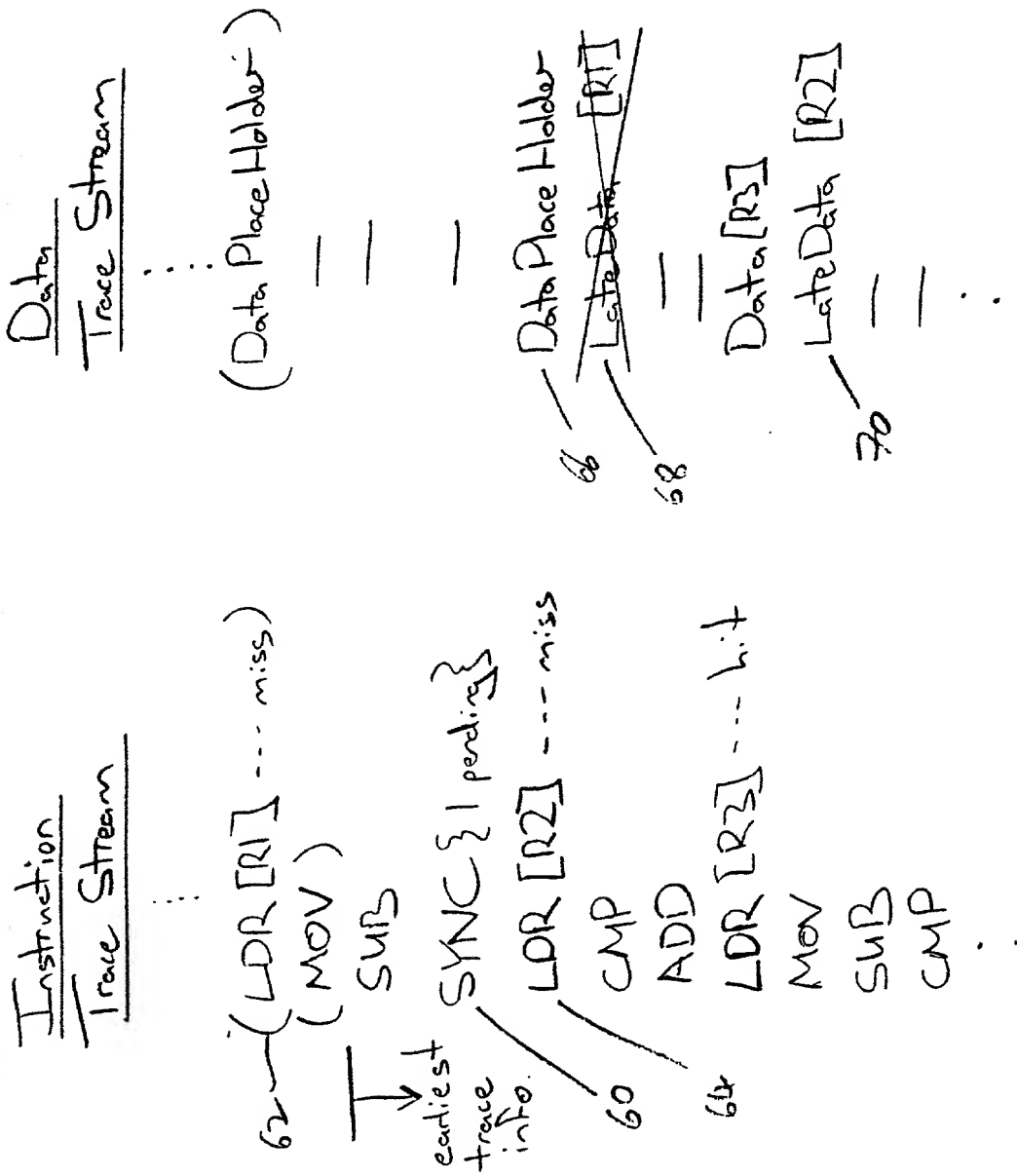


Fig. 5

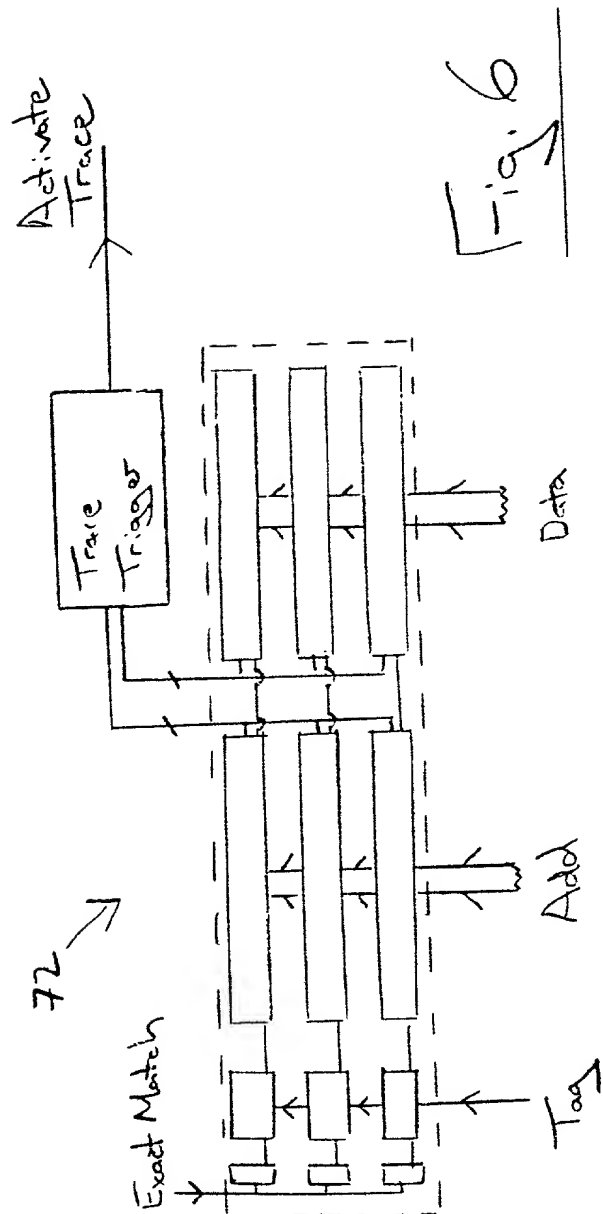


Fig. 6

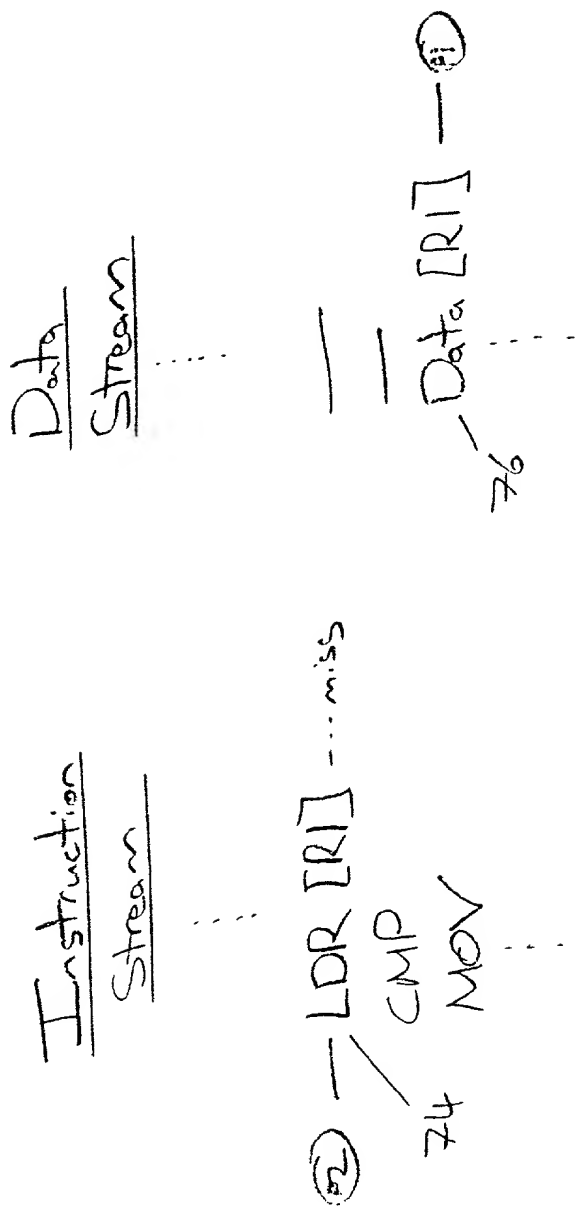


Fig. 7

Exact Match	Add Match	Data Match	Trace Activation Point
✓	✓	✓	①
✓	✓	✗	none
✗	✓	✓	②
✗	✓	✗	②

Fig 8

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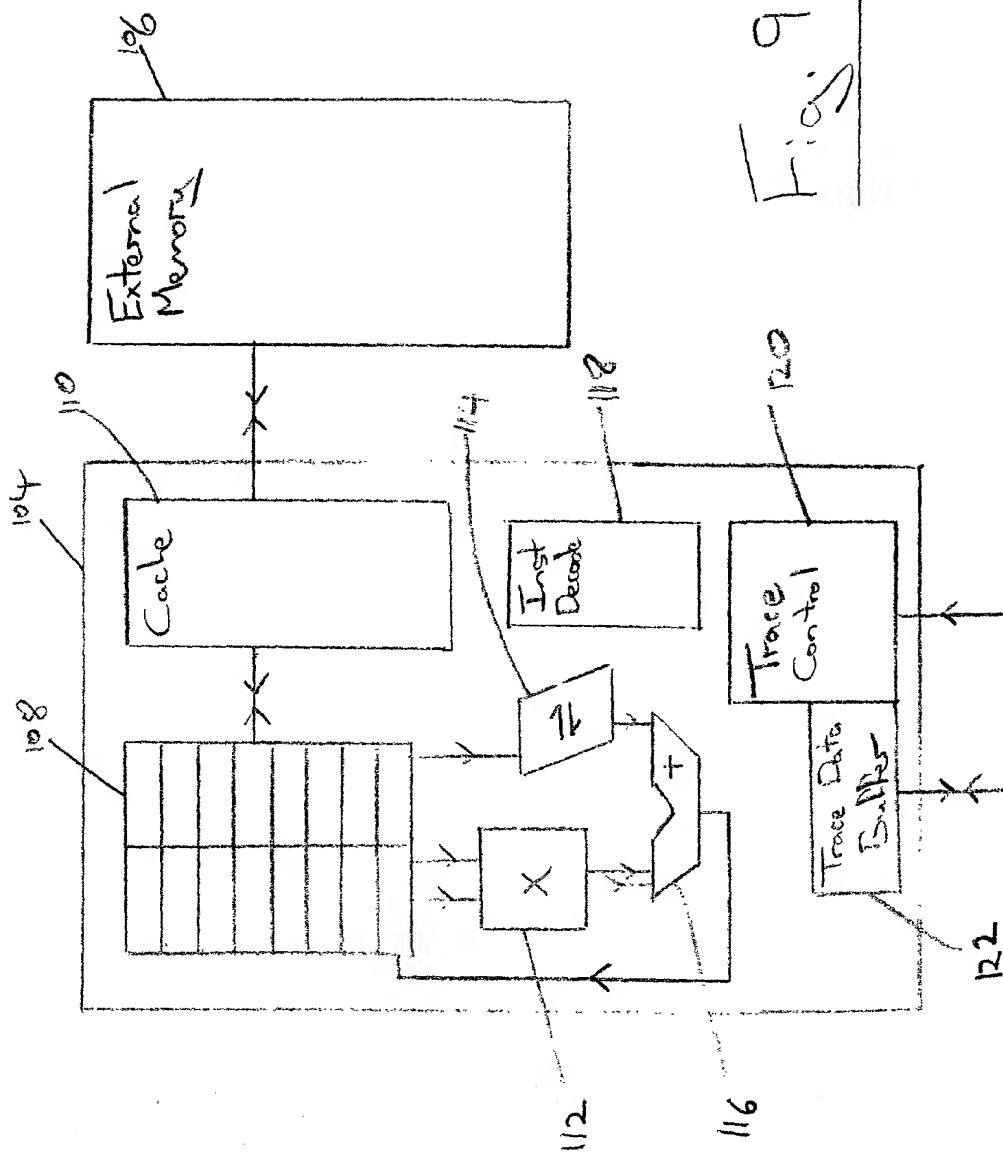


Fig. 9

LSM $R_n[Add]$, $RegList$

16 bits

0000 1000 1000 1011



R0	
R1	
R2	R11
R3	
R4	
R5	
R6	
R7	

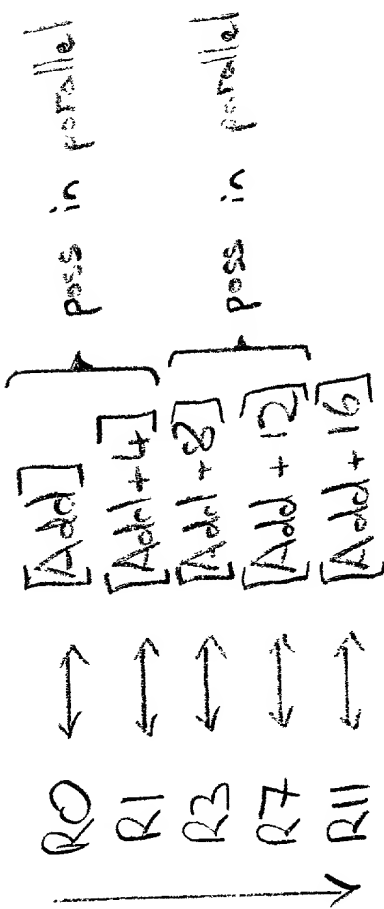
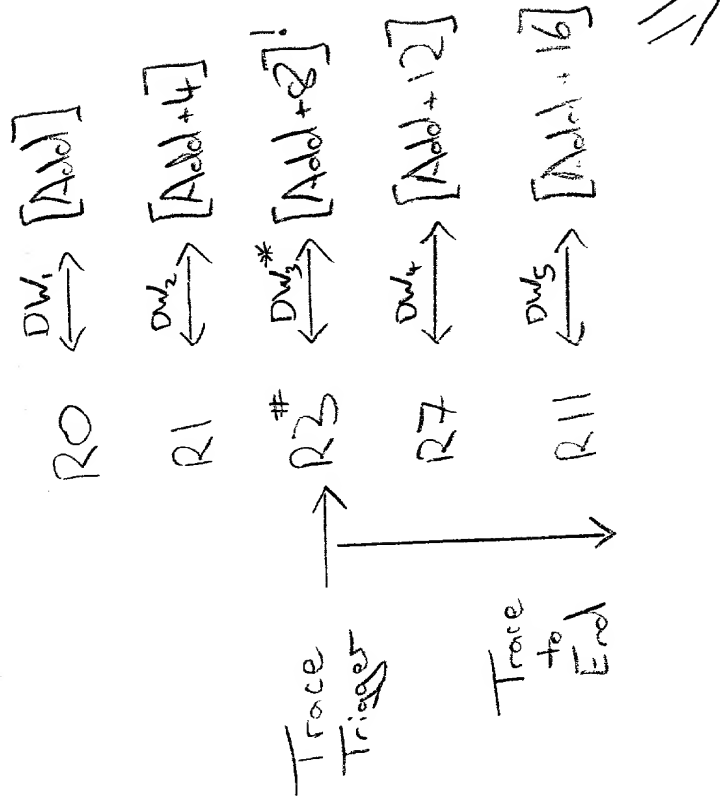


Fig. 10



Trace Data Stream

$\dots, \{LSM\}, \{Add+8\}, \{DW_3\}, \{Place\ Hold\}, \{Place\ Hold\}, \dots$

← Count back to $R3$

Fig. 11

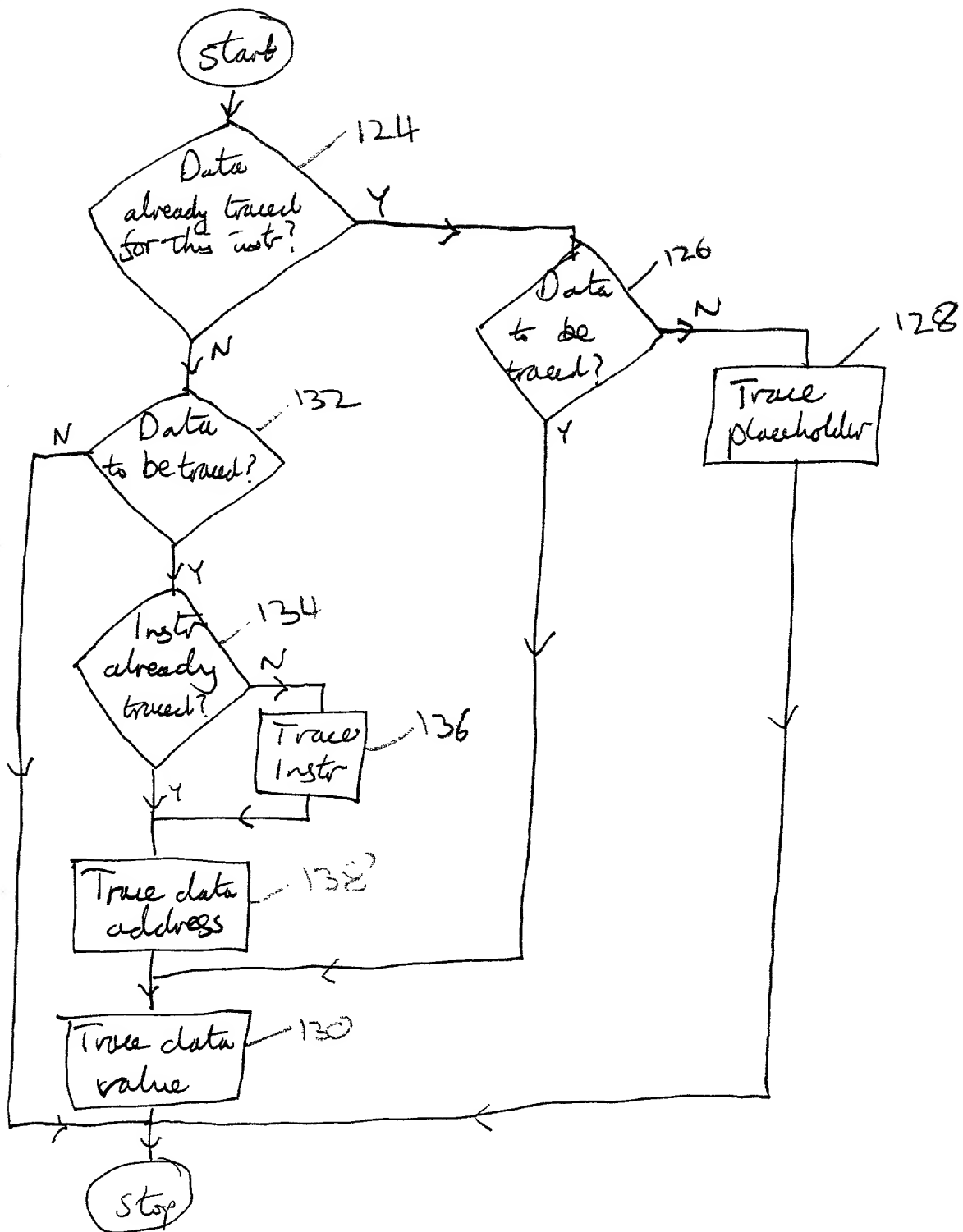


Fig. 12

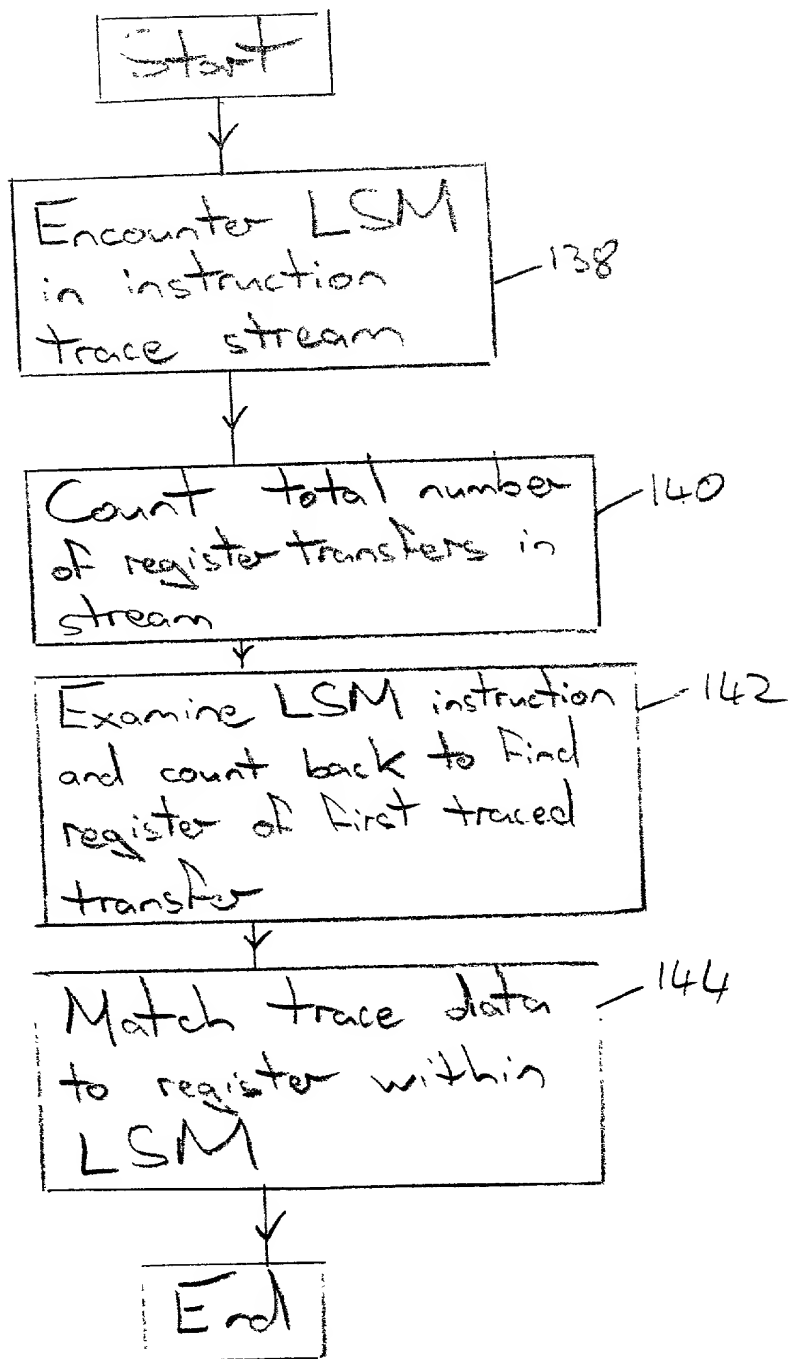


Fig. 13